

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|---------------------------------------|---|------------------------------|
| In re the Application of: |) | Group Art Unit: Not Assigned |
| |) | |
| Dornisch, et al. |) | Examiner: Not Assigned |
| |) | |
| Serial No.: Not Assigned |) | |
| |) | |
| Filed: Herewith |) | |
| |) | |
| For: High-K Dielectric Stack in a MIM |) | |
| Capacitor and Method for its |) | |
| Fabrication |) | |
| |) | |
| |) | |

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

In accordance with 37 C.F.R. 1.56 and 1.97, Applicant hereby discloses to the Patent Office patents, publications or other information of which Applicant is aware. A copy of the patents and other materials along with a Form 1449 are submitted herewith.

The items identified in this Information Disclosure Statement may or may not be "material" as defined in 37 C.F.R. 1.56 and the submission thereof by Applicant is not to be construed as an admission that any such patent, publication or other information referred to is material or considered to be material (37 C.F.R. 1.97(h)), or even qualifies as "prior art" under 35 U.S.C. 102 with respect to the present invention unless specifically designated by Applicant as such. Identification of any reference or patent herein is not an admission, nor is it to be construed as an admission, that it was invented prior to the invention disclosed herein.


The filing of this Information Disclosure Statement is not to be construed to mean that a search has been made or that no other material information, as defined in 37 C.F.R. 1.56, exists.

In accordance with 37 C.F.R. 1.97(b)(3) no fee is enclosed, since the Information Disclosure Statement is being filed before the mailing date of a first office action on the merits.

The Commissioner is hereby authorized to charge or credit Deposit Account Number 50-0731 for any deficiency or overpayment in the fees required for the filing of this Disclosure Statement, for which purpose a duplicate copy of this paper is also included.

Respectfully Submitted;
FARJAMI & FARJAMI LLP

Dated: 10/22/03

By: 
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Lori Llave

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| FORM PTO-1449 LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | ATTY. DOCKET NO. 0150141 | SERIAL NO. Not Assigned |
| | APPLICANTS: Dornisch, et al. | |
| | FILING DATE: Herewith | GROUP ART: Not Assigned |

| U.S. PATENT DOCUMENTS | | | | | | | |
|-----------------------|----|-----------------|------|------|-------|--------------|----------------|
| Exam. Initials | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE |
| | AA | | | | | | |
| | AB | | | | | | |
| | AC | | | | | | |

| FOREIGN PATENT DOCUMENT | | | | | | | | |
|-------------------------|----|-----------------|---------------------|--------------------------------|-------|--------------|-------------|----|
| Exam. Initials | | DOCUMENT NUMBER | PUBLICATION DATE | COUNTRY OR PATENT OFFICE | CLASS | SUB CLASS | TRANSLATION | |
| | | | | | | | YES | NO |
| | BA | | | | | | | |
| | BB | | | | | | | |
| | BC | | | | | | | |

| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) | | |
|--|----|--|
| | CA | Won, et al., "Novel Plasma Enhanced Atomic Layer Deposition Technology for High-k Capacitor with EOT of 8Å on Conventional Metal Electrode"; 2003 Symposium on VLSI Technology Digest of Technical Papers, 4-89114-035-6/03. |
| | CB | |

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|---|-------------------------|
| EXAMINER: | DATE CONSIDERED: |
| EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant. | |
| Information Disclosure Statement -- Section 9 PTO-1449 | |
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